

FIG. 1A

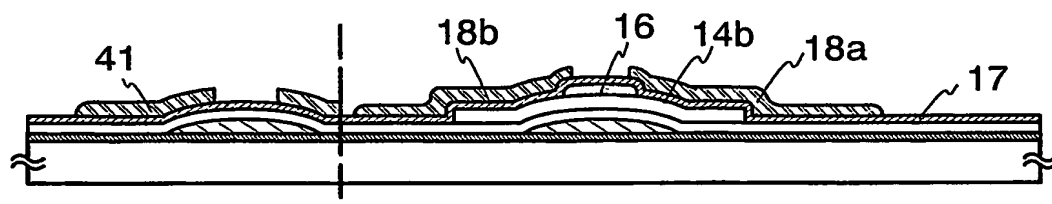


FIG. 1B

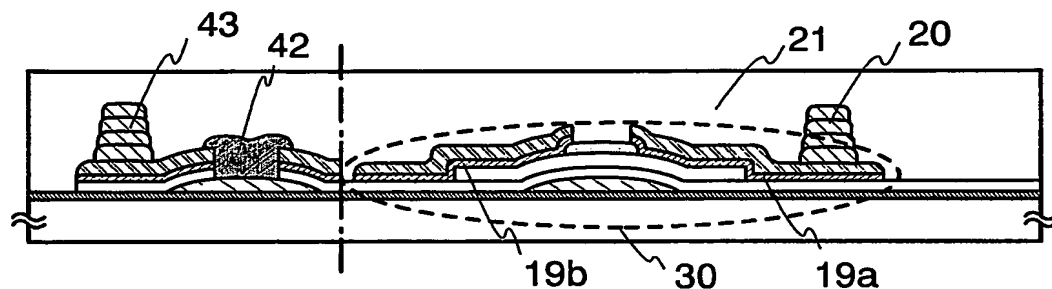


FIG. 1C

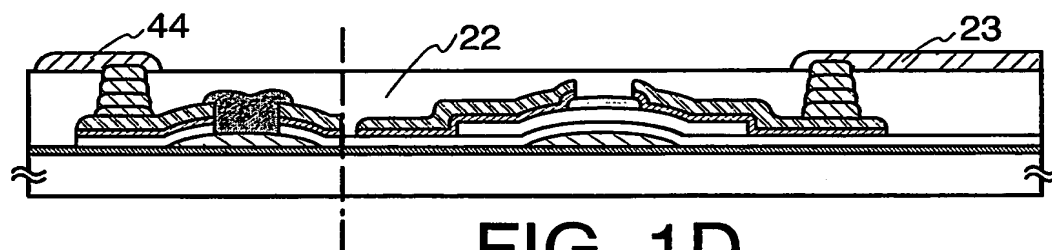


FIG. 1D

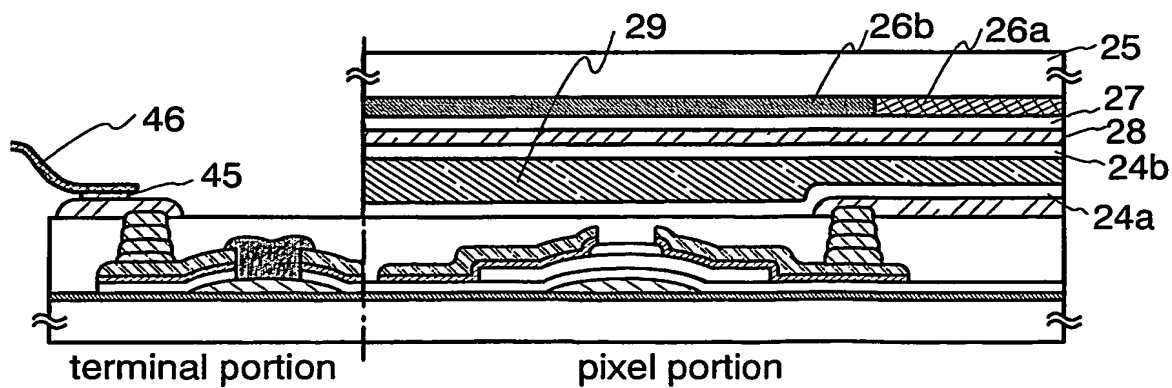


FIG. 1E

2/22

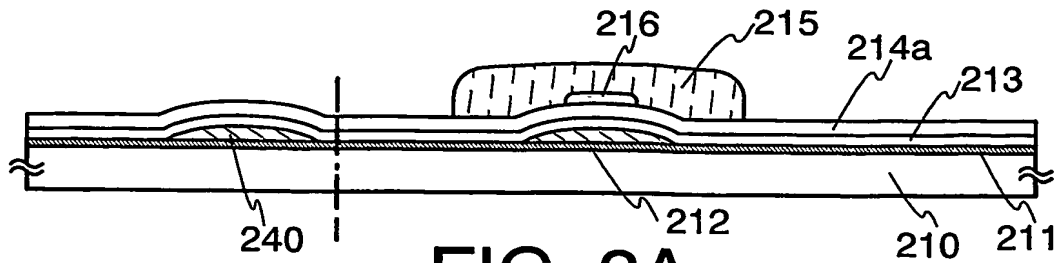


FIG. 2A

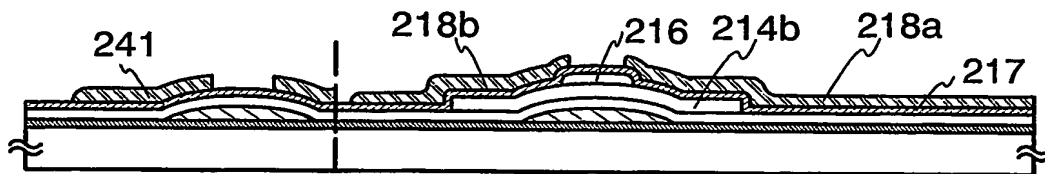


FIG. 2B

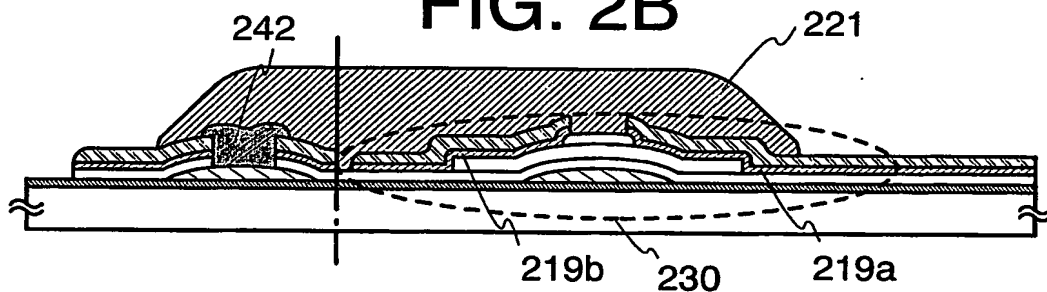


FIG. 2C

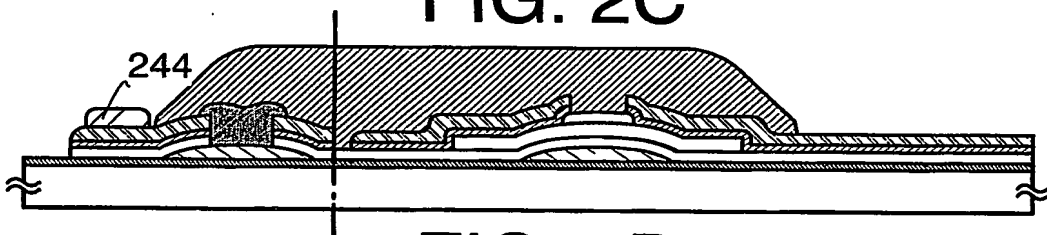


FIG. 2D

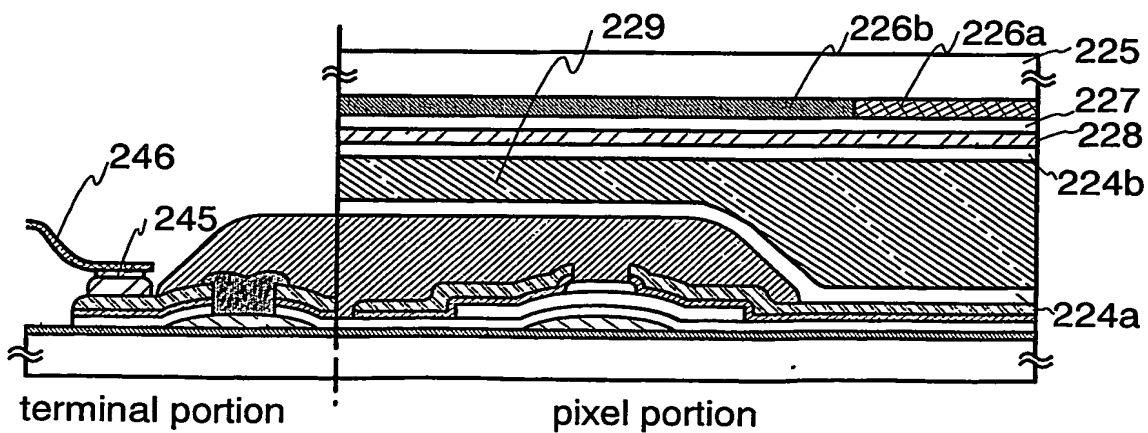


FIG. 2E

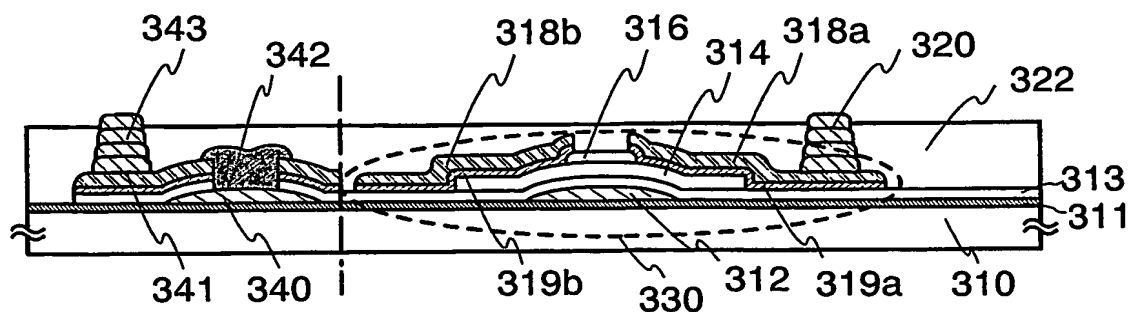


FIG. 3A

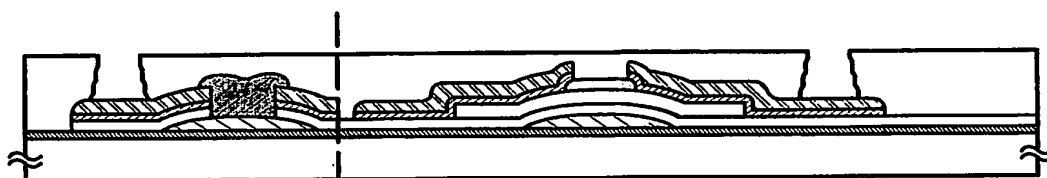


FIG. 3B

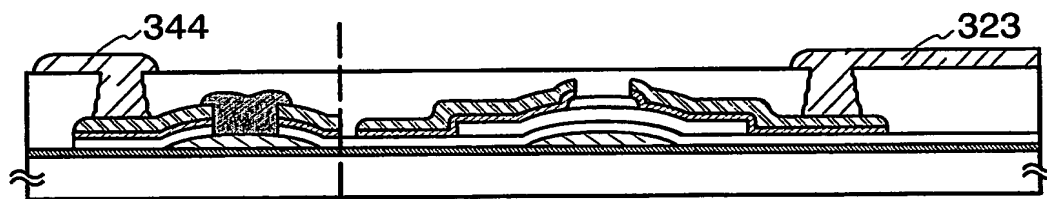


FIG. 3C

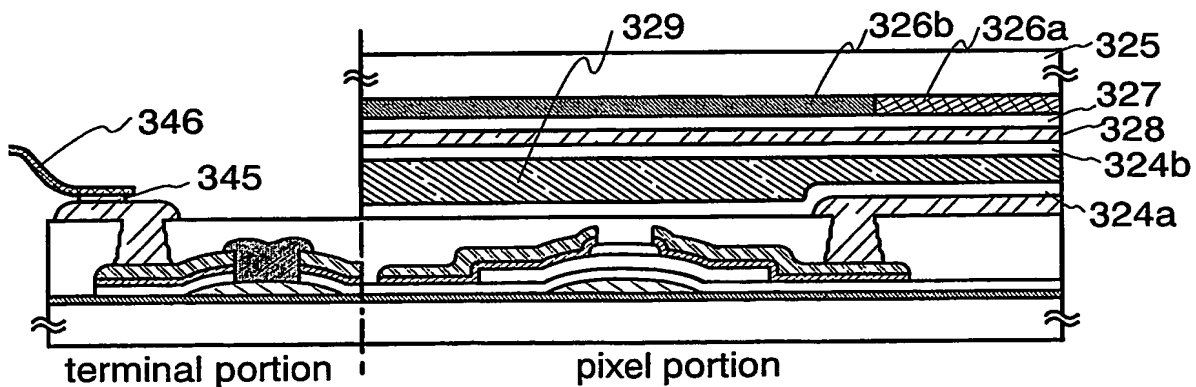


FIG. 3D

4/22

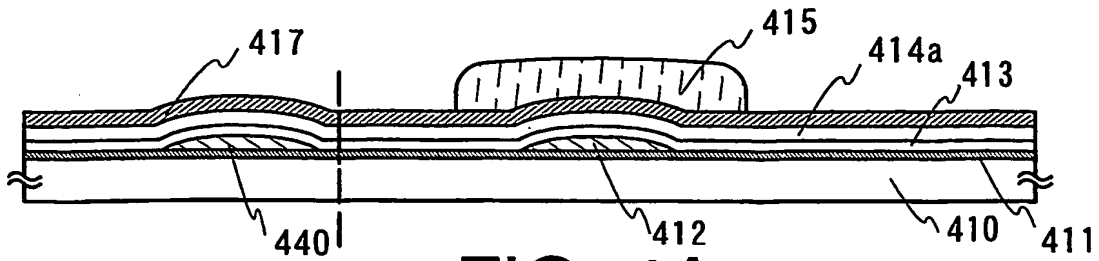


FIG. 4A

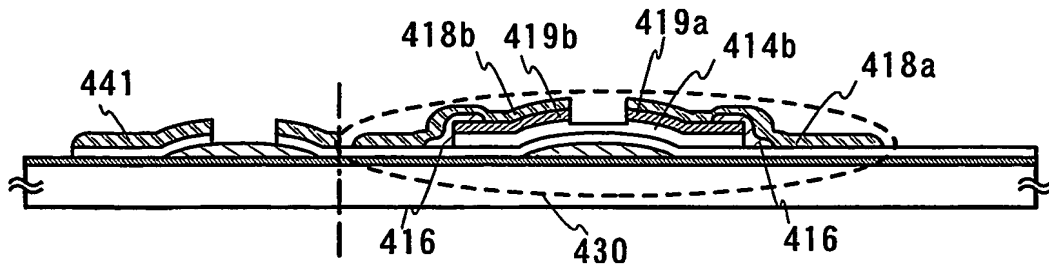


FIG. 4B

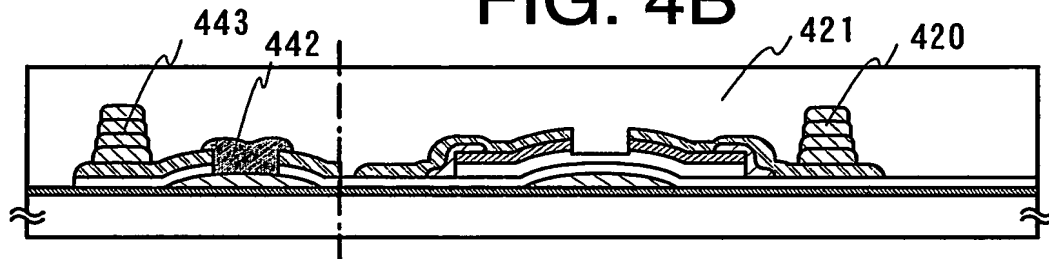


FIG. 4C

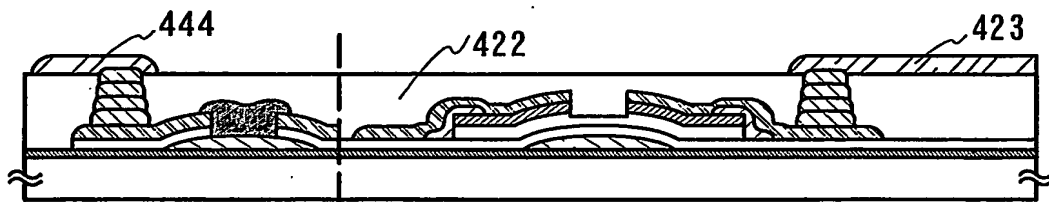


FIG. 4D

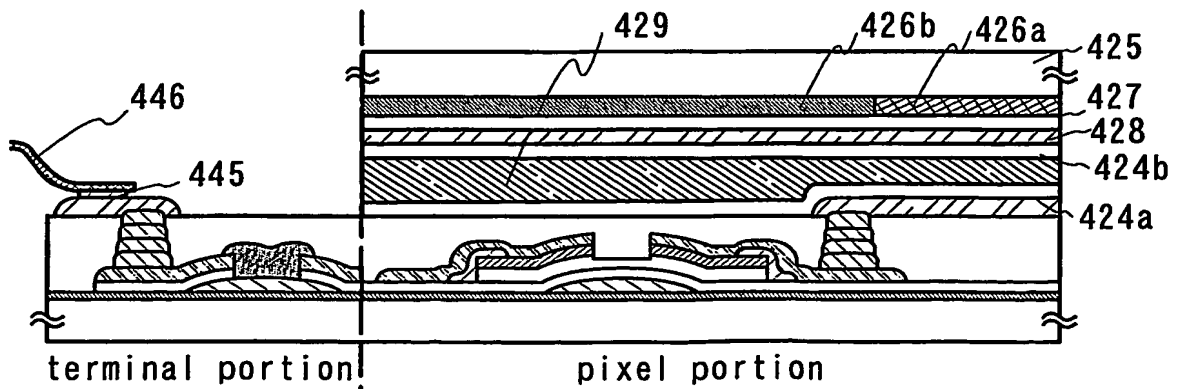


FIG. 4E

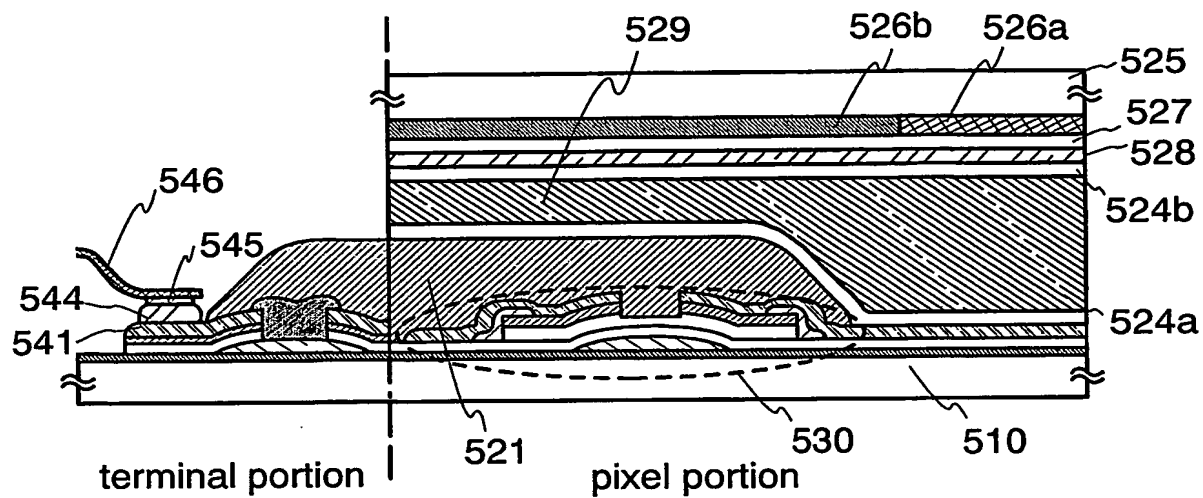


FIG. 5

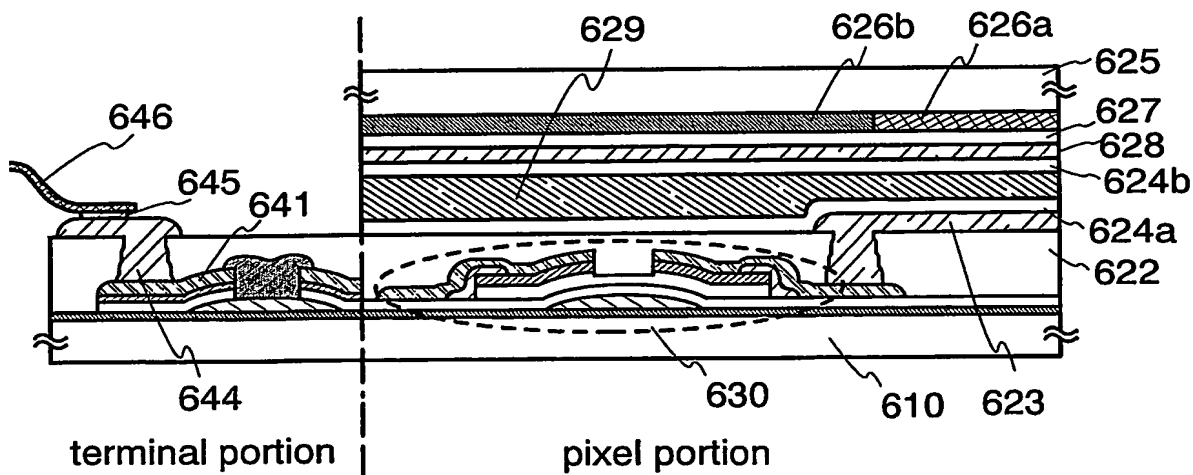


FIG. 6

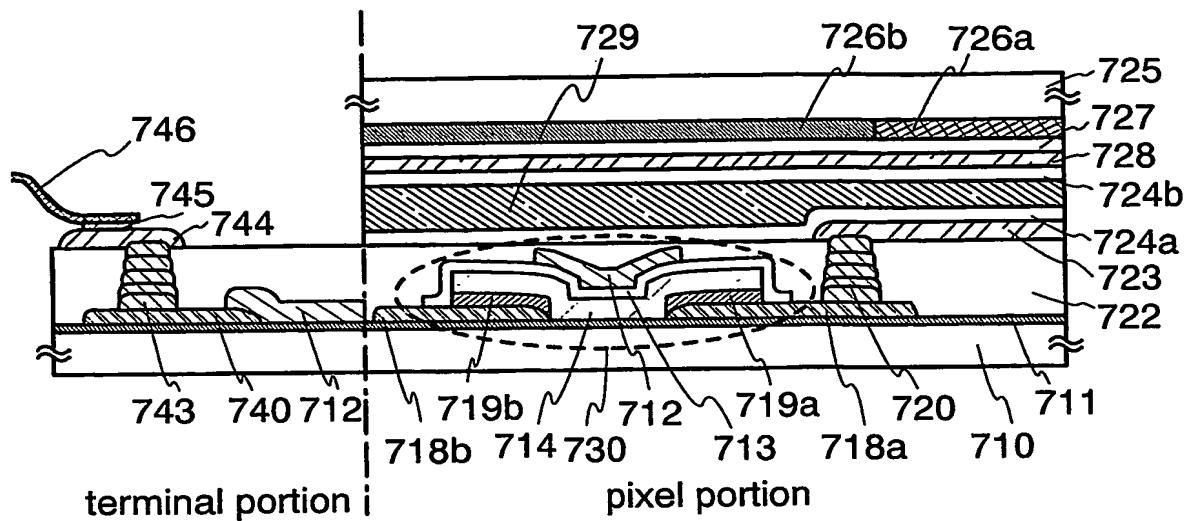


FIG. 7

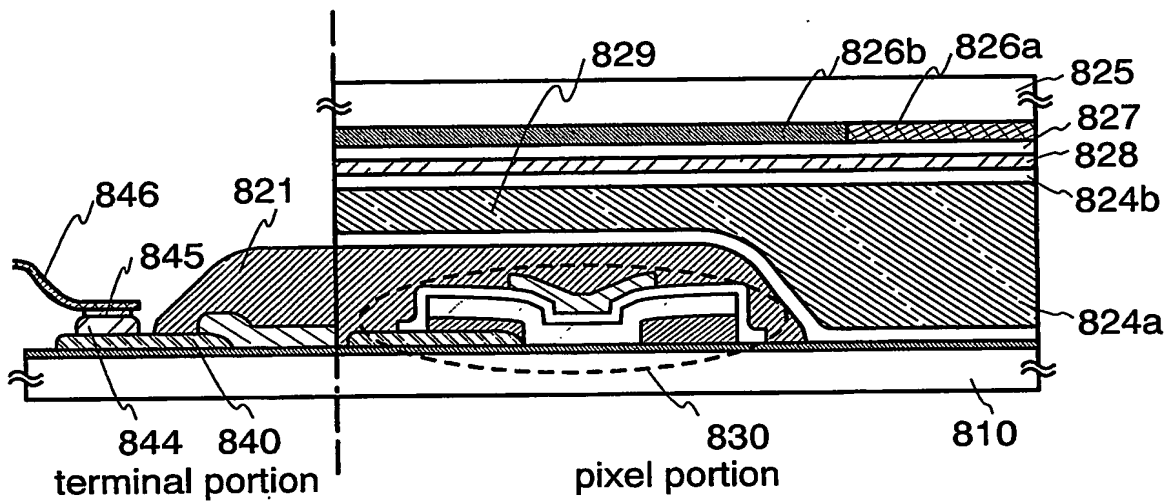


FIG. 8

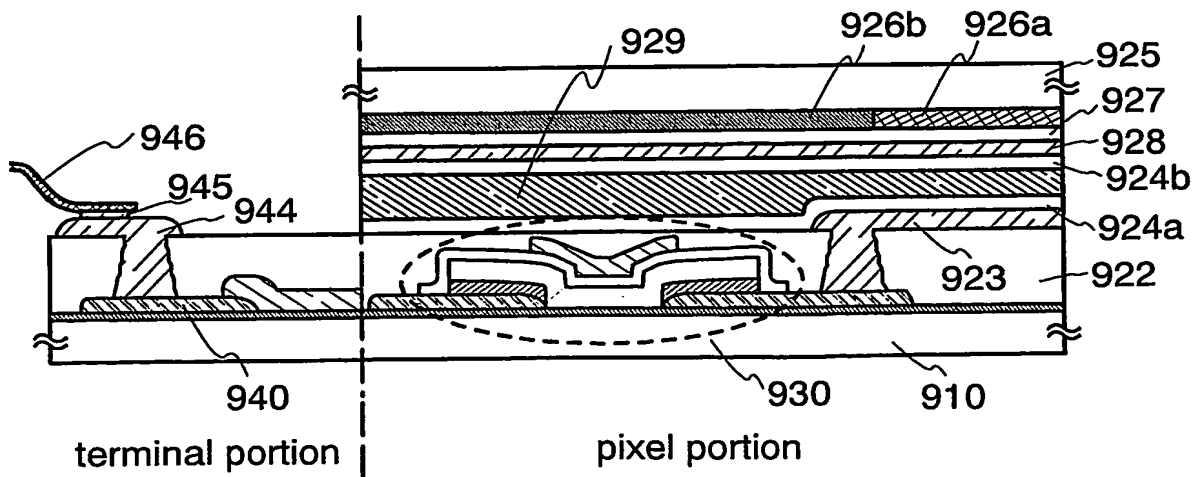


FIG. 9

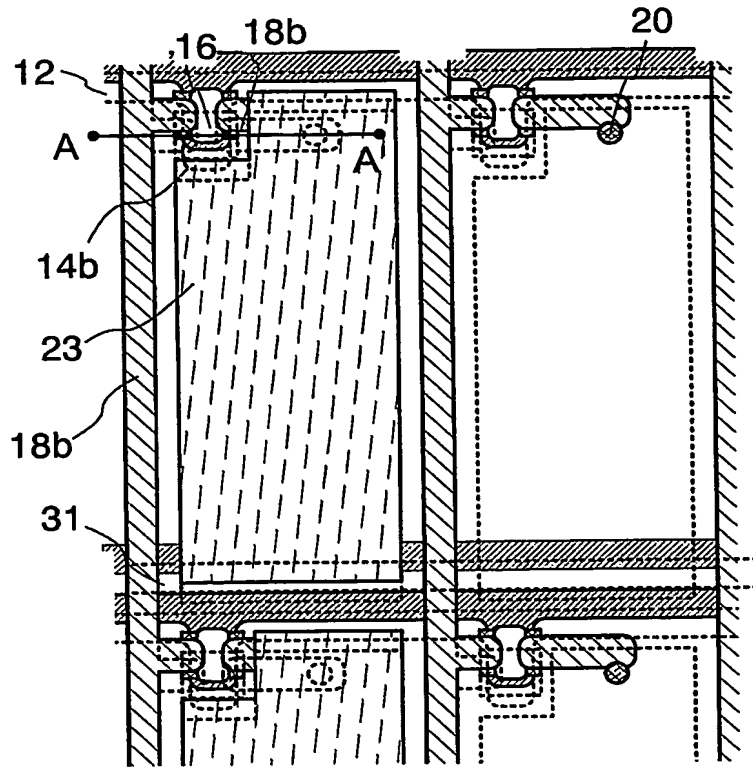


FIG. 10

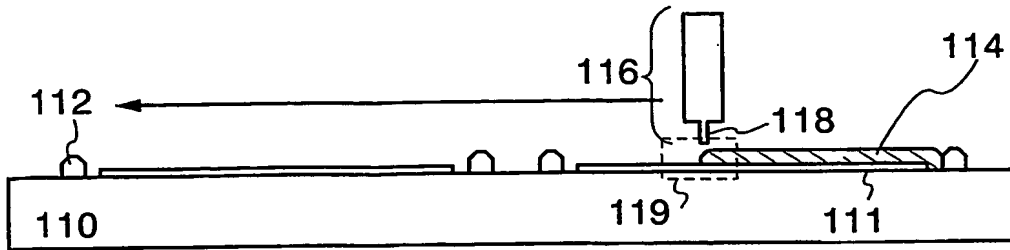


FIG. 11A

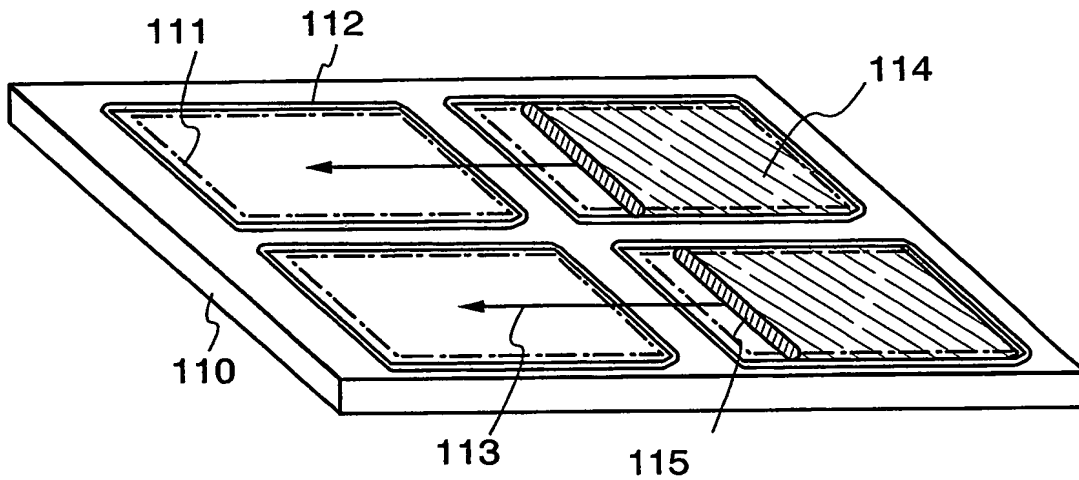


FIG. 11B

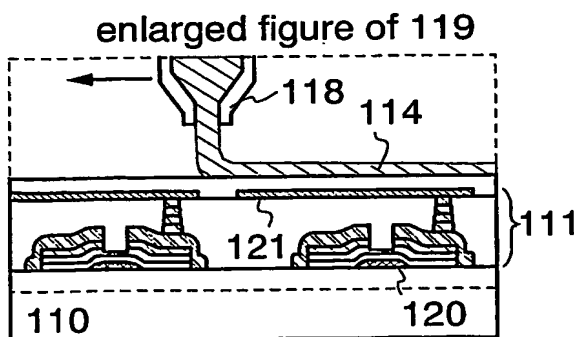


FIG. 11C

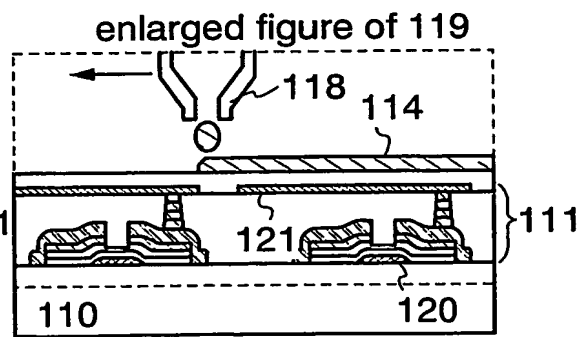


FIG. 11D

9/22

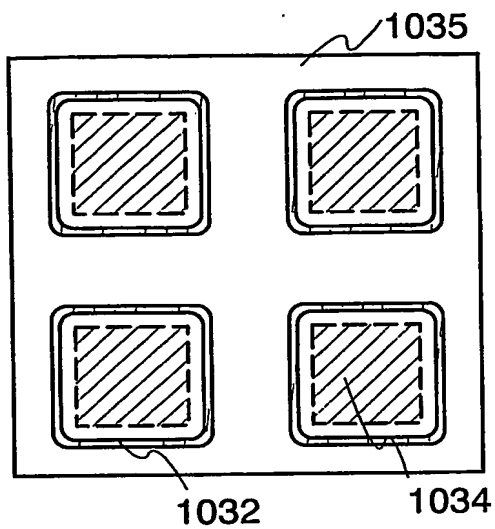


FIG. 12A

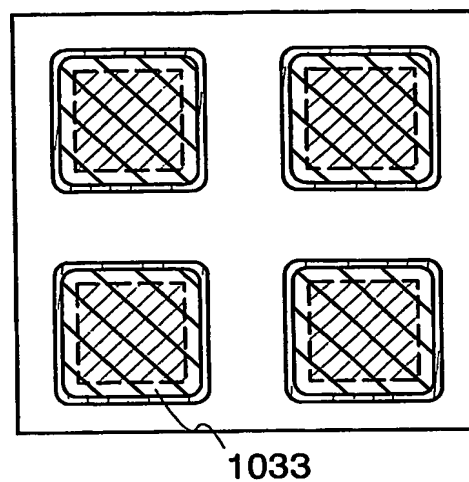


FIG. 12B

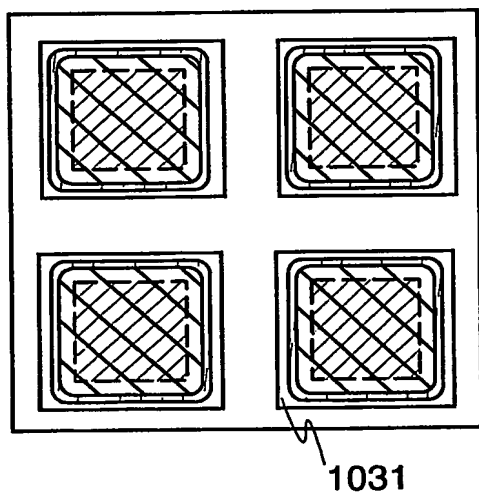


FIG. 12C

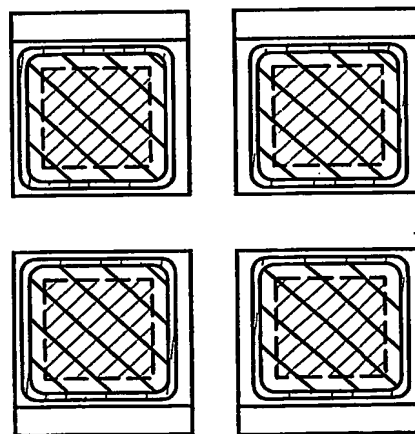


FIG. 12D

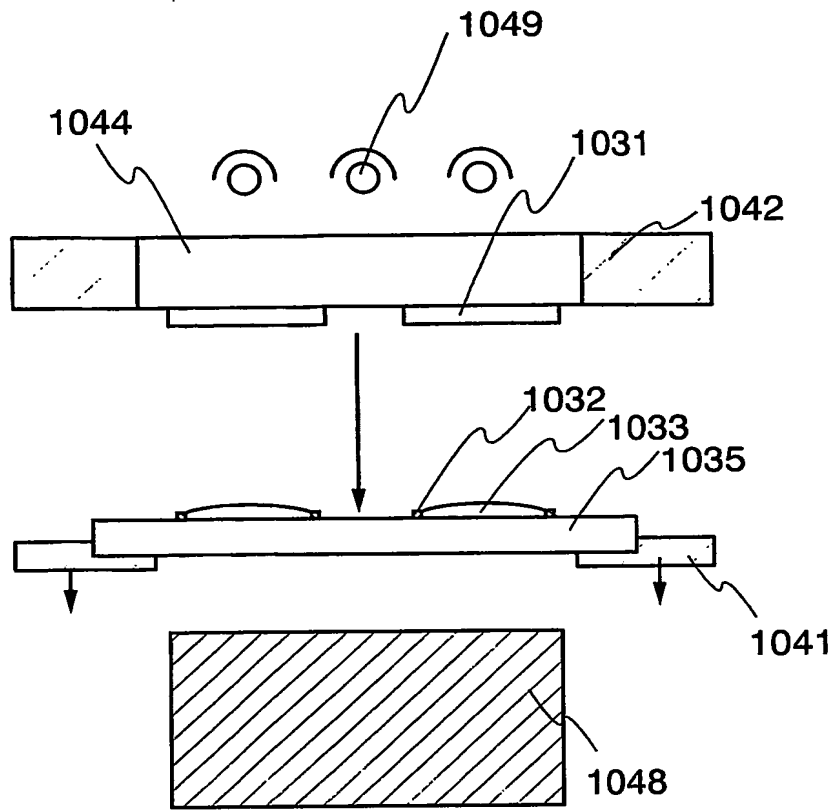


FIG. 13A

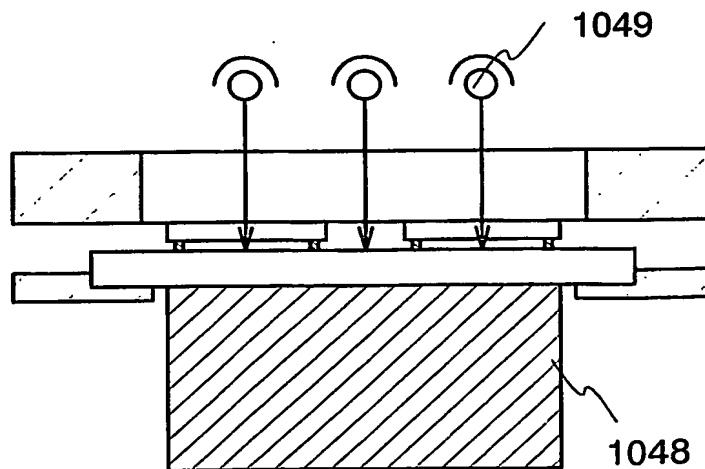


FIG. 13B

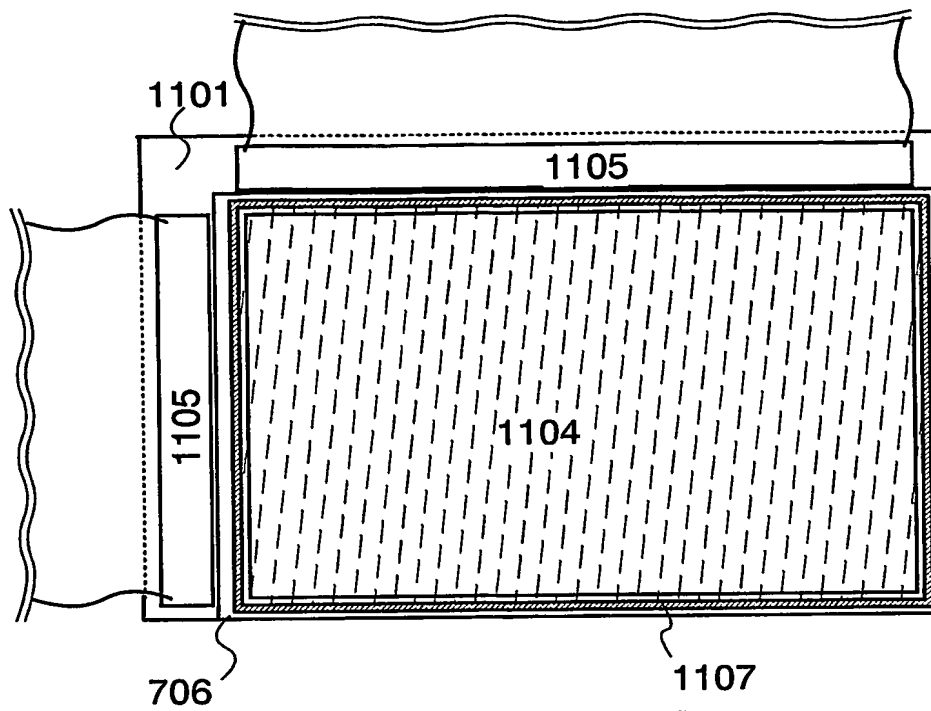


FIG. 14A

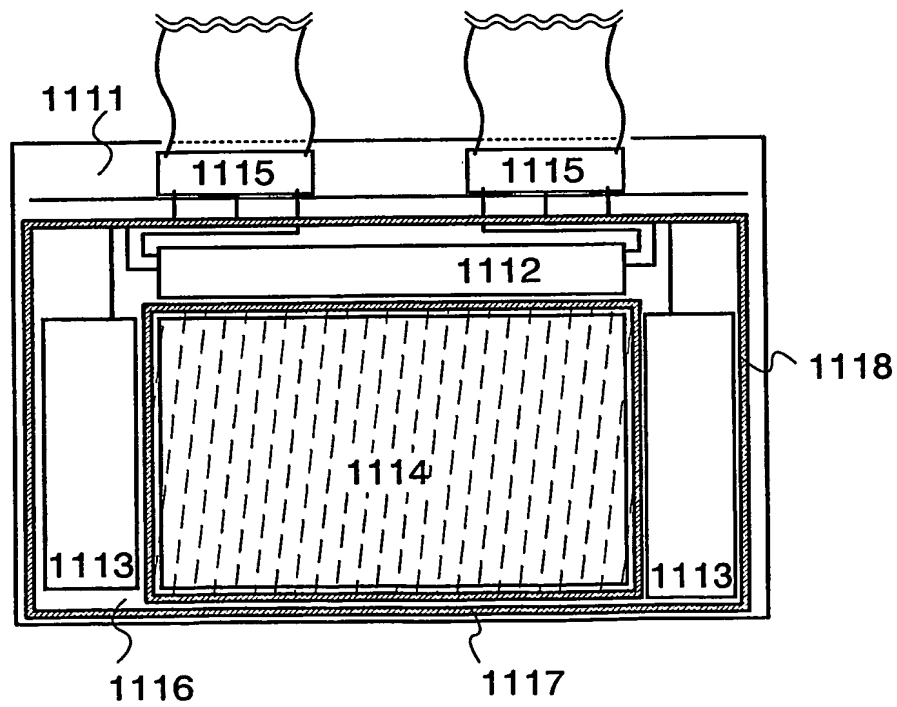


FIG. 14B

12/22

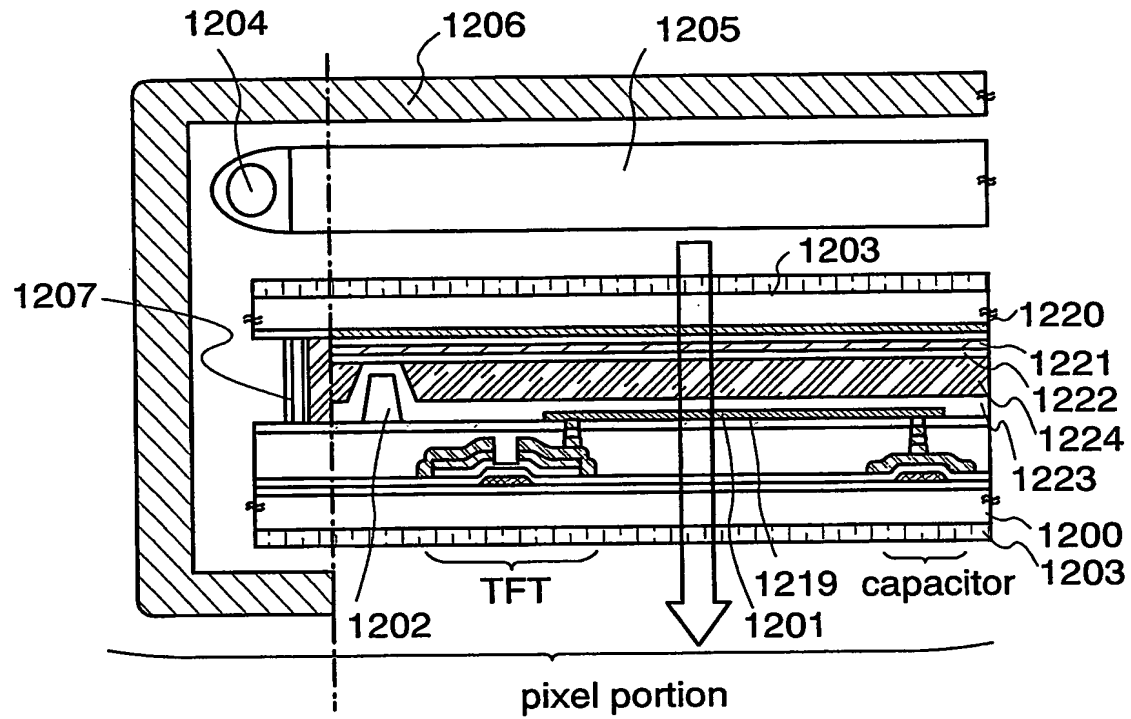
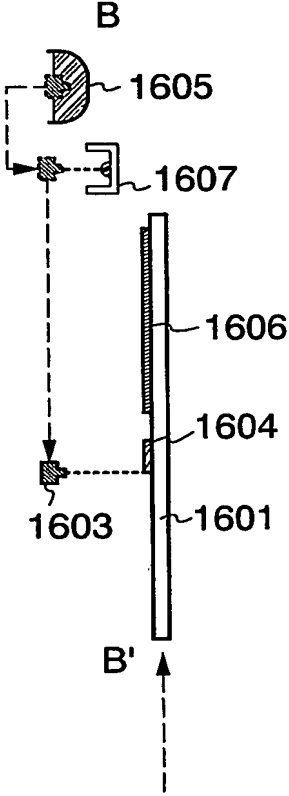
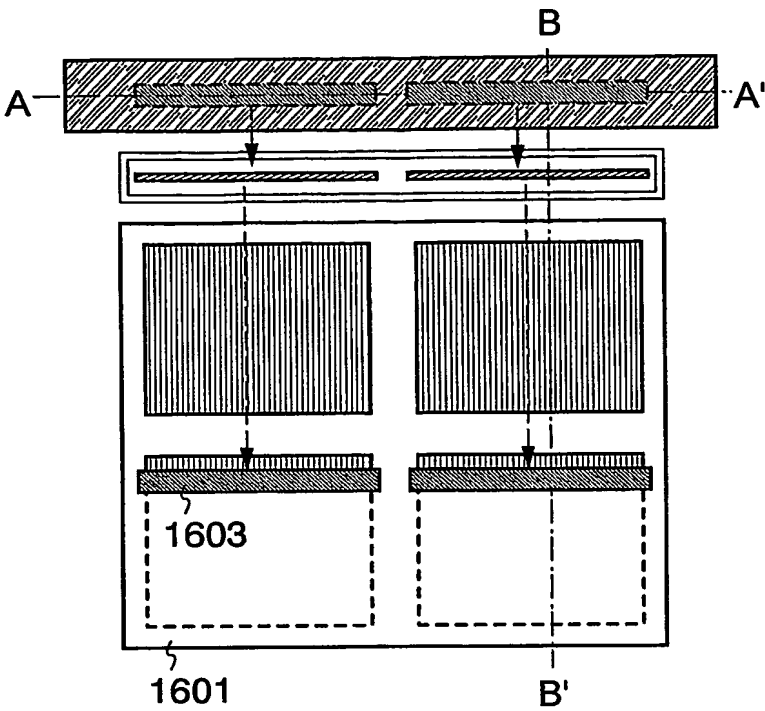


FIG. 15



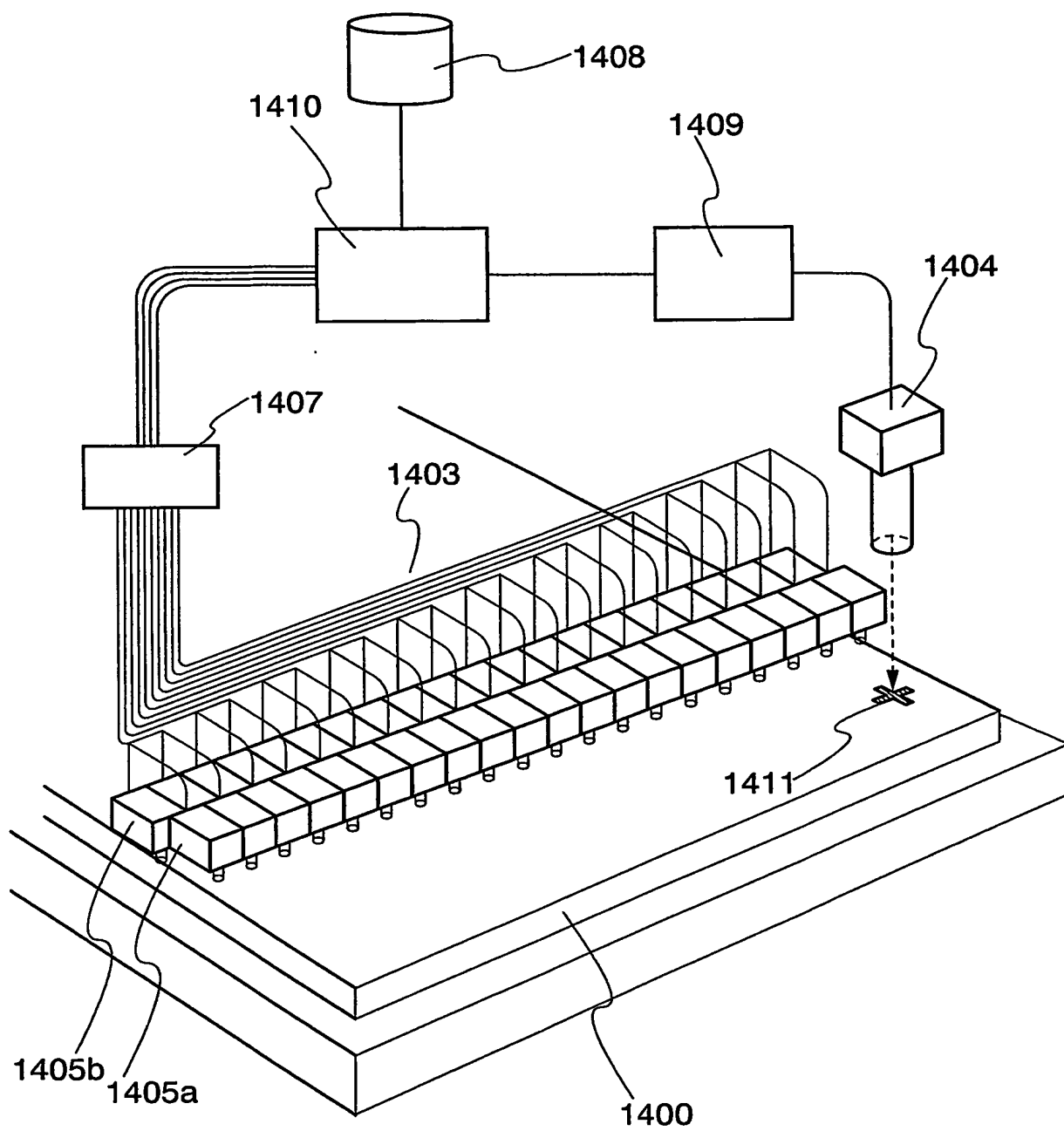


FIG. 17

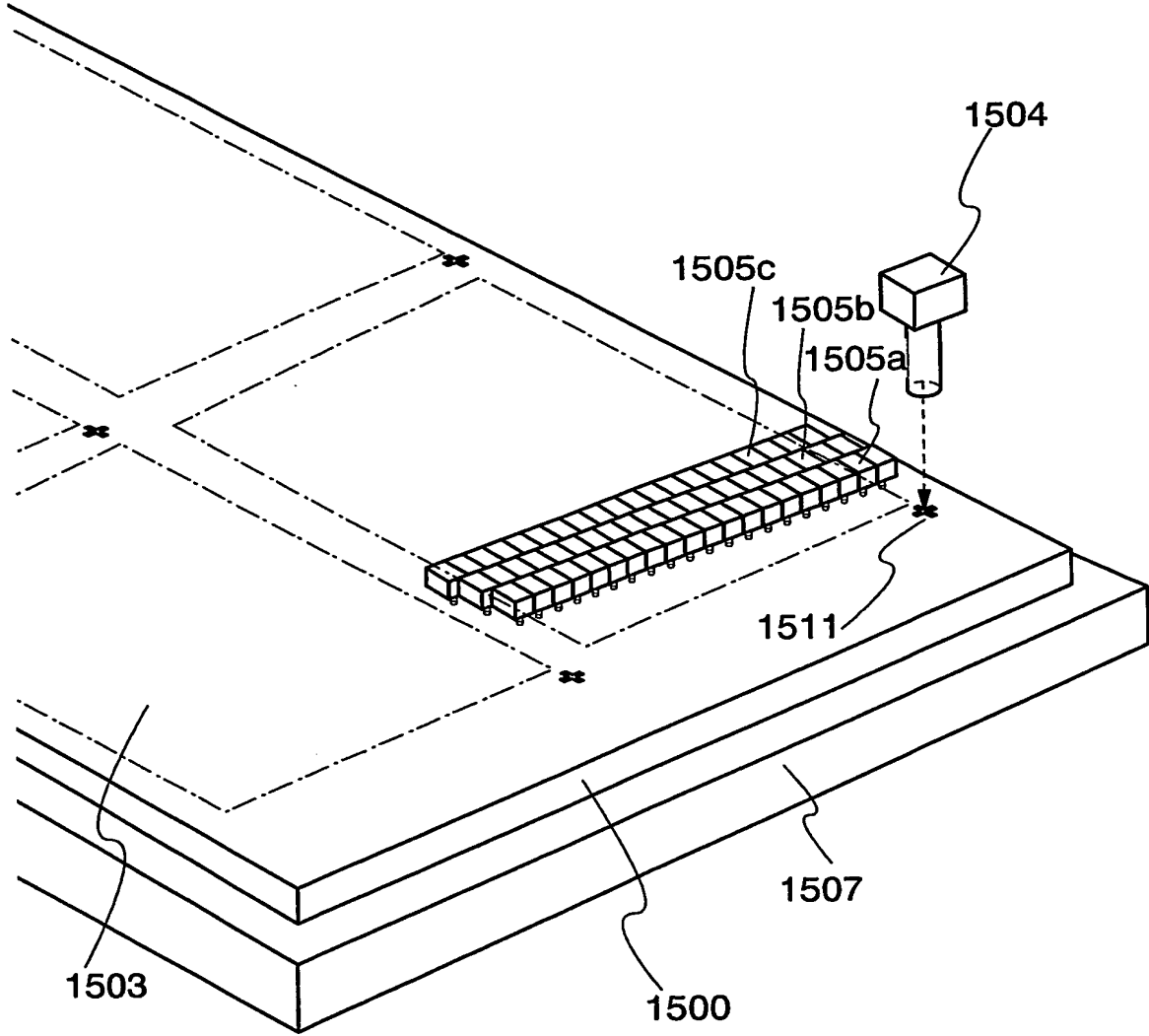


FIG. 18

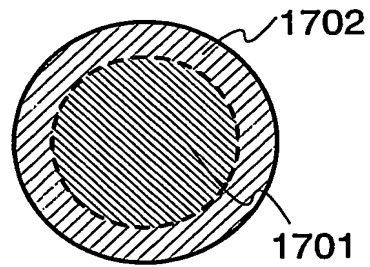


FIG. 19A

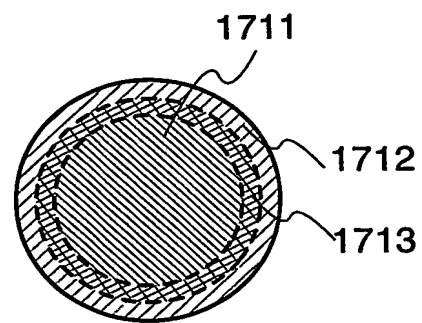


FIG. 19B

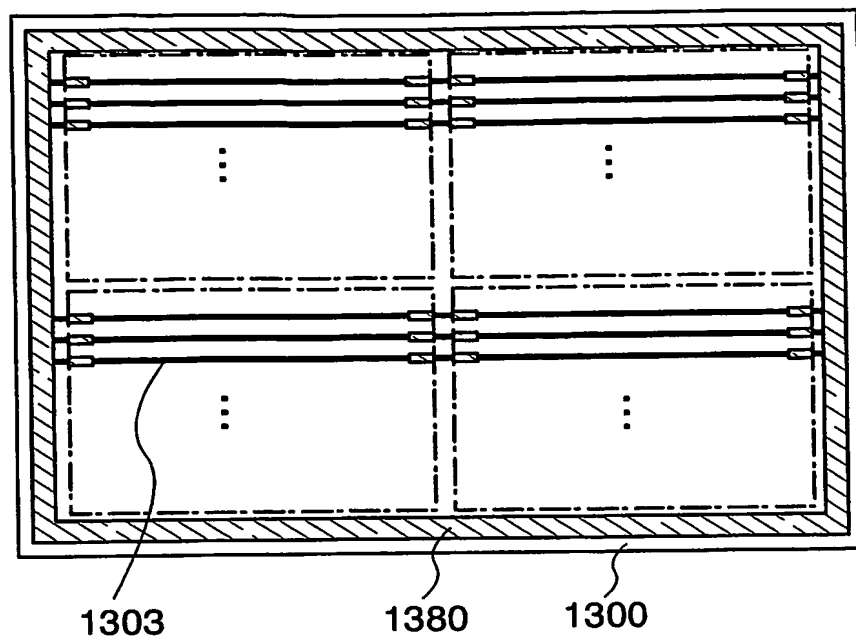


FIG. 20A

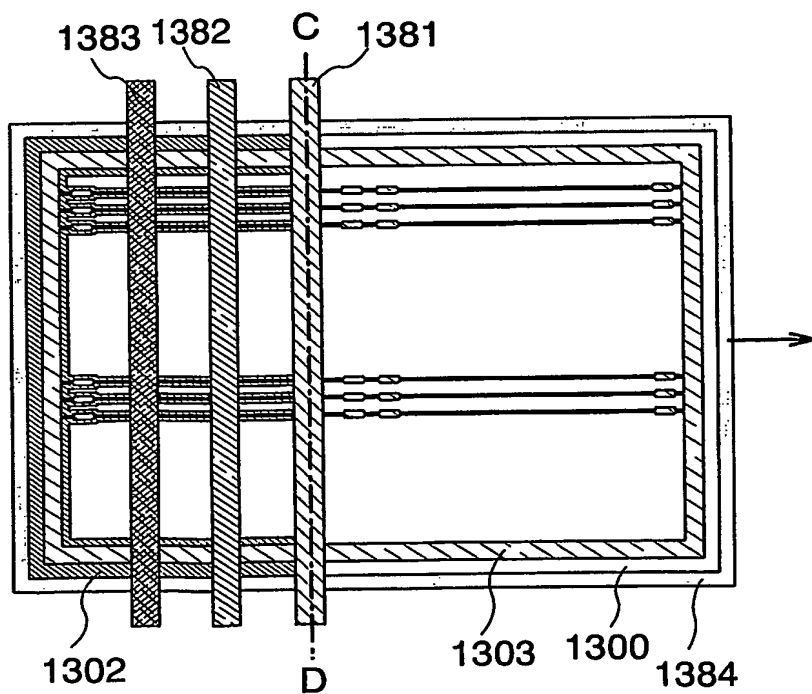


FIG. 20B

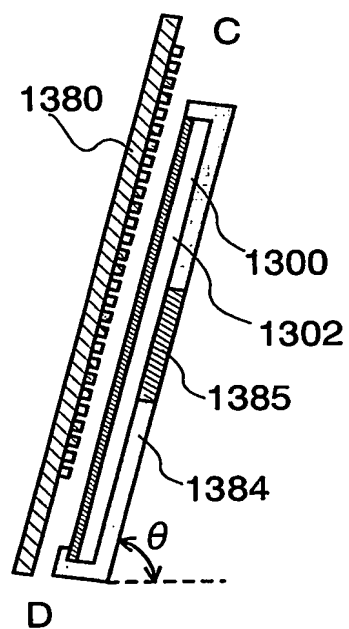


FIG. 20C

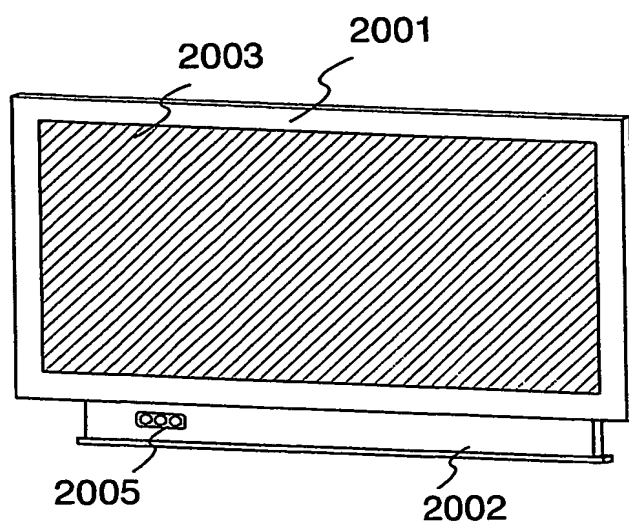


FIG. 21A

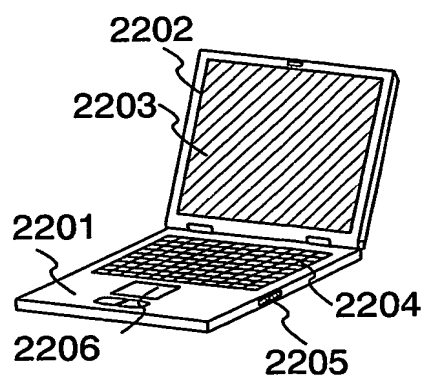


FIG. 21B

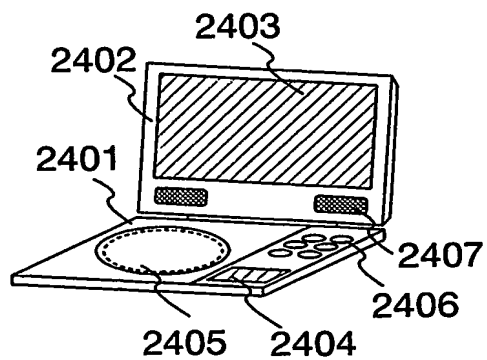


FIG. 21C

19/22

EXPLANATION OF REFERENCE

10 a substrate; 11 a base film; 12 a metal wiring; 13 a gate insulating film; 14a a semiconductor film; 14b a semiconductor layer; 15 a mask; 16 an insulating layer; 17 an n-type semiconductor film; 18a a source or drain wiring; 18b a source or drain wiring; 19a a source or drain region; 19b a source or drain region; 20 a projection; 21 an interlayer insulating film; 22 an interlayer insulating film; 23 a pixel electrode; 24a an alignment layer; 24b an alignment layer; 25 a counter substrate; 26a a color layer; 26b a light shielding layer; 27 an overcoat layer; 28 a counter electrode; 29 liquid crystal; 30 a TFT; 31 a capacitor wiring; 40 a wiring; 41 a connection wiring; 42 a conductor; 43 a projection; 44 a terminal electrode; 45 an anisotropic conductive layer; 46 an FPC; 110 a large substrate; 111 a pixel area; 112 a sealant; 113 a nozzle operating direction; 114 a liquid crystal material; 115 an application surface; 116 a droplet discharge system ;118 a nozzle; 119 an area surrounded with dotted lines; 120 an inverted staggered TFT; 121 a pixel electrode; 210 a substrate; 211 a base film ;213 a gate insulating film; 214a a semiconductor film ;214b a semiconductor layer; 215 a mask; 216 an insulating layer; 217 an n-type semiconductor film; 218a a source or drain wiring; 218b a source or drain wiring; 219a a source or drain region; 219b a source or drain region; 221 an interlayer insulating film; 224a an alignment layer; 224b an alignment layer; 225 a counter substrate; 226a a color layer; 226b a light shielding layer; 227 an overcoat layer; 228 a counter electrode; 229 liquid crystal; 230 a TFT; 240 a wiring; 241 a connection wiring; 242 a conductor; 244 a terminal electrode; 245 an anisotropic conductive layer; 246 an FPC; 310 a substrate; 314 a semiconductor layer; 316 an insulating layer; 318a a source or drain wiring; 318b a source or drain wiring; 319a a source or drain region; 319b a source or drain region; 320 a projection; 322 an interlayer insulating film; 323 a pixel electrode; 324a an alignment layer;

324b an alignment layer; 325 a counter substrate; 326a a color layer; 326b a light shielding layer; 327 an overcoat layer; 328 a counter electrode; 329 liquid crystal; 330 a TFT; 340 a wiring; 341 a connection wiring; 342 a conductor; 343 a projection; 344 a terminal electrode; 345 an anisotropic conductive layer; 346 an FPC; 410 a substrate; 411 a base film; 412 a metal wiring; 413 a gate insulating film; 414a a semiconductor film; 414b a semiconductor layer; 415 a mask; 416 a layer formed of an insulating material or a conductive material; 417 an n-type semiconductor film ; 418a a source or drain wiring; 418b a source or drain wiring; 419a an n-type semiconductor film; 419b an n-type semiconductor film; 420 a projection; 421 an interlayer insulating film; 422 an interlayer insulating film; 423 a pixel electrode; 424a an alignment layer; 424b an alignment layer; 425 a counter substrate; 426a a color layer; 426b a light shielding layer; 427 a overcoat layer; 428 a counter electrode; 429 liquid crystal; 430 a TFT; 440 a wiring; 441 a connection wiring; 442 a conductor; 443 a projection; 444 a terminal electrode; 445 an anisotropic conductive layer; 446 an FPC; 510 a substrate; 521 an interlayer insulating film; 524a an alignment layer; 524b an alignment layer; 525 a counter substrate; 526a a color layer; 526b a light shielding layer; 527 an overcoat layer; 528 a counter electrode; 529 liquid crystal; 530 a TFT; 544 a terminal electrode; 545 an anisotropic conductive layer; 546 an FPC; 610 a substrate; 622 an interlayer insulating film; 623 a pixel electrode; 624a an alignment layer; 624b an alignment layer; 625 a counter substrate; 626a a color layer; 626b a light shielding layer; 627 an overcoat layer; 628 a counter electrode; 629 liquid crystal; 630 a TFT; 641 a connection wiring; 644 a terminal electrode; 645 an anisotropic conductive layer; 646 an FPC; 706 a counter substrate; 710 a substrate; 711 a base film; 712 a gate wiring; 713 a gate insulating layer; 714 a semiconductor layer; 718a a source or drain wiring; 718b a source or drain wiring; 719a an n-type semiconductor layer ; 719b an n-type semiconductor layer; 720 a projection; 722 an

interlayer insulating film; 723 a pixel electrode; 724a an alignment layer; 724b an alignment layer; 725 a counter substrates; 726a a color layer; 726b a light shielding layer; 727 an overcoat layer; 728 a counter electrode; 729 liquid crystal; 730 a TFT; 740 a terminal electrode; 743 a projection; 744 a terminal electrode; 745 an anisotropic conductive layer; 746 an FPC; 824a an alignment layer; 824b an alignment layer; 825 a counter substrate; 826a a color layer; 826b a light shielding layer; 827 an overcoat layer; 828 a counter electrode; 829 liquid crystal; 830 a TFT; 840 a connection wiring; 844 a terminal electrode; 845 an anisotropic conductive layer; 846 an FPC; 924a an alignment layer; 924b an alignment layer; 925 a counter substrate; 926a a color layer; 926b a light shielding layer; 927 an overcoat layer; 928 a counter electrode; 929 liquid crystal; 930 a TFT; 940 a connection wiring; 944 a terminal electrode; 945 an anisotropic conductive layer; 946 an FPC; 1031 a second substrate; 1032 a sealant; 1033 liquid crystal; 1034 a pixel area; 1035 a first substrate; 1041 a first substrate holder; 1042 a second substrate holder; 1044 a window; 1048 bottom plate; 1049 a light source; 1101 a substrate; 1104 a pixel area; 1105 an FPC; 1107 a sealant; 1111 a substrate; 1112 a source signal line driver circuit; 1113 a gate signal line driver circuit; 1114 a pixel area; 1115 an FPC; 1116 a counter substrate; 1117 a first sealant; 1118 a second sealant; 1200 a substrate; 1201 a pixel electrode; 1202 a spacer; 1203 a polarizer; 1204 a backlight; 1205 an optical waveguide; 1206 a cover; 1207 a sealant; 1219 a protective film; 1220 a color filter; 1221 a counter electrode; 1222 an alignment layer; 1223 an alignment layer; 1224 a liquid crystal layer; 1300 a substrate; 1302 a conductive film; 1303 a gate electrode; 1380 a conductive film; 1381 a head; 1382 a head; 1383 a head; 1384 a stage; 1385 a stage; 1400 a substrate; 1403 a droplet discharge means; 1404 an imaging means; 1405a a head; 1405b a head; 1407 a control means; 1408 a storage medium; 1409 an image processing means; 1410 a computer; 1411 a marker; 1500 a large

substrate; 1503 an area; 1504 an imaging means; 1505a a head; 1505b a head; 1505c a head; 1507 a stage; 1511 a marker; 1601 a substrate; 1603 a head portions; 1604 a composition; 1605 solvent container; 1606 a composition; 1607 a test stage; 1701 copper; 1702 silver; 1711 copper; 1712 silver; 1713 a buffer layer; 2001 a chassis; 2002 a support; 2003 a display area; 2005 a video input terminal; 2201 a main body; 2202 a chassis; 2203 a display area; 2204 a keyboard; 2205 an external connection port; 2206 a pointing mouse; 2401 a main body; 2402 a chassis; 2403 a display area A; 2404 a display area B; 2405 a recording medium reading part; 2406 an operation key; 2407 a speaker unit